

MCUBoot Tools Release Notes

1 Overview

These are the release notes for MCUBoot tools.

This release includes the PC-hosted blhost application. The application is used for downloading firmware to Flash device in both development and production phase. This release also includes elftosb command-line application and elftosb-gui companion tool. It is used to generate bootable image for the device ROM.

2 Development tools

The host tools were compiled and tested with following development tools.

Host projects:

- Microsoft Visual Studio® Professional 2015 for Windows® OS Desktop
- Microsoft Visual Studio C++ Redistributable for Visual Studio 2015 (vc_redist_x86.exe)
- Apple Xcode® v9.2 (for tools).
- Linux® OS GNU Compiler (GCC) v5.4.0, libstdc++6, libudev-dev, libc6, and libgcc1 for the Linux build.
- Linux OS tools have been tested on Ubuntu 16.04 LTS (GLIBC v2.23).
- Apple Mac® OS host tools have been tested on MacOS High Sierra v10.13.3.

3 System requirements

System requirements are based on the requirements for the development tools. The recommended PC configuration is 2 GHz processor, 2 GB RAM, and 2 GB free disk space.

Windows OS applications like blhost require installation of Visual C++ redistributable 2015 or greater.

4 Target requirements

This release of the tools supports the following platforms:

- LPCXpresso55S69
- EVK-MIMXRT685

Contents

1 Overview.....	1
2 Development tools.....	1
3 System requirements.....	1
4 Target requirements	1
5 Release contents.....	2
6 Getting started.....	2
7 Host tools.....	2
8 Revision history.....	2



- EVK-MIMXRT595

There are no specific requirements for the hardware other than what the board requires to operate.

5 Release contents

Table 1 describes the release contents

Table 1. Release contents

Deliverable	Location
Host binaries and utilities	blhost application is under <sdk_package>/middleware/mcu-boot/bin/Tools/blhost folder elftosb application is under <sdk_package>/middleware/mcu-boot/bin/Tools/elftosb folder elftosb-gui application is under <sdk_package>/middleware/mcu-boot/bin/Tools
Documentation	<sdk_package>/middleware/mcu-boot/doc
Tools build projects	<sdk_package>/middleware/mcu-boot/tools/tools...

6 Getting started

To understand the steps required to use the host tools to generate a user application boot image and load it to external flash device, see the blhost User's Guide, elftosb User's Guide, and elftosb-gui User's Guide under /doc directory.

7 Host tools

The bootloader release contains the binaries for the following PC-based host tools:

- elftosb: command line tool to convert ELF/SREC formatted application image into bootable image format (or SB format).
- blhost: command line tool to perform application programming.
- elftosb-gui: GUI tool to simplify the use of elftosb.

All the utilities are available for Windows, Linux, and Mac operating systems.

8 Revision history

This is the first revision of the document.

Table 2. Revision history

Revision number	Date	Substantive changes
0	05/2018	Initial Release
1	05/2018	Updates for EVK- MIMXRT595

How To Reach Us

Home Page:

nxp.com

Web Support:

nxp.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: nxp.com/SalesTermsandConditions.

While NXP has implemented advanced security features, all products may be subject to unidentified vulnerabilities. Customers are responsible for the design and operation of their applications and products to reduce the effect of these vulnerabilities on customer's applications and products, and NXP accepts no liability for any vulnerability that is discovered. Customers should implement appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, COOLFLUX, EMBRACE, GREENCHIP, HITAG, I2C BUS, ICODE, JCOP, LIFE VIBES, MIFARE, MIFARE CLASSIC, MIFARE DESFire, MIFARE PLUS, MIFARE FLEX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TRENCHMOS, UCODE, Freescale, the Freescale logo, Altivec, C-5, CodeTEST, CodeWarrior, ColdFire, ColdFire+, C-Ware, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorIQ, QorIQ Qonverge, Ready Play, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, SMARTMOS, Tower, TurboLink, and UMEMS are trademarks of NXP B.V. All other product or service names are the property of their respective owners. AMBA, Arm, Arm7, Arm7TDMI, Arm9, Arm11, Artisan, big.LITTLE, Cordio, CoreLink, CoreSight, Cortex, DesignStart, DynamIQ, Jazelle, Keil, Mali, Mbed, Mbed Enabled, NEON, POP, RealView, SecurCore, Socrates, Thumb, TrustZone, ULINK, ULINK2, ULINK-ME, ULINK-PLUS, ULINKpro, μ Vision, Versatile are trademarks or registered trademarks of Arm Limited (or its subsidiaries) in the US and/or elsewhere. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org.

© NXP B.V. 2019.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 05/2019

Document identifier: MCUBOOTTOOLRN

